

ABSTRACT

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The present invention is a striping disk controller used in a computer system that includes a CPU connected to a system bus and running an operating system and having a BIOS. A disk drive interface is connected to the system bus and communicates with the BIOS. First and second disk drives are provided, each including analog electronics, data separator electronics, formatting electronics and head positioning electronics. A striping controller is connected between the first and second disk drives and the interface. The striping controller causes data being communicated between the system bus and the first and second drives to be written and read to and from the first and second drives substantially in parallel, alternating sectors on each drive. Striping increases data transfer rates by accessing two drives simultaneously. During read and write operations, both drives are accessed simultaneously which has the effect of doubling the effective transfer rate and capacity.

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